

2043
1413
Access DB# 142595
SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: Todd Ingberg Examiner #: 75084 Date: 11/15/05
Art Unit: 2124 Phone Number: 2-3723 Serial Number: 091058, 291
Mail Box and Bldg/Room Location: RND 51709 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover-sheet, pertinent claims, and abstract.

Title of Invention: _____

Inventors (please provide full names): _____

Earliest Priority Filing Date: May 2001

For Sequence Searches Only Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

Program Example Wanted - Part of a compiler

A how to write an Access Violation Routine
it raises an "exception handler."

Best mode since AVR are common is an
implentation in checking a LOOP control Variable
also called an "invariant." checks typically for
being "out of bounds"
GNU website or open source

STAFF USE ONLY

	Type of Search	Vendors and cost where applicable
Searcher: <u>Geoffrey St. Ledger</u>	NA Sequence (#) _____	STN _____
Searcher Phone #: <u>23540</u>	AA Sequence (#) _____	Dialog <input checked="" type="checkbox"/>
Searcher Location: <u>41831</u>	Structure (#) _____	Questel/Orbit _____
Date Searcher Picked Up: <u>1/26/5</u>	Bibliographic <input checked="" type="checkbox"/>	Dr. Link _____
Date Completed: <u>1/28/5</u>	Litigation _____	Lexis/Nexis _____
Searcher Prep & Review Time: <u>60</u>	Fulltext _____	Sequence Systems _____
Clerical Prep Time: _____	Patent Family _____	WWW/Internet <input checked="" type="checkbox"/>
Online Time: <u>230</u>	Other _____	Other (specify) <u>ACM</u>



[> home](#) [> about](#) [> feedback](#) [> login](#)

US Patent & Trademark Office



Try the *new* Portal design

Give us your opinion after using it.

Search Results

Search Results for: **[invariant* <near/10> ((range* or series or sequence* or succession or successive or consecutive or array* or group* or cluster* or set* or collection* or family or families*) <near/7> (address or addresses))]**

Found **13** of **148,786** searched.

Search within Results



[> Advanced Search](#)

[> Search Help/Tips](#)

Sort by: **Title** **Publication** **Publication Date** **Score** Binder

Results 1 - 13 of 13 **short listing**

- 1** Optimization for a superscalar out-of-order machine 100%

Anne M. Holler

Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture December 1996

Compiler optimization plays a key role in unlocking the performance of the PA-8000, an innovative dynamically-scheduled machine which is the first implementation of the 64-bit PA 2.0 member of the HP PA-RISC architecture family. This wide superscalar, long out-of-order machine provides significant execution bandwidth and automatically hides latency at runtime; however, despite its ample hardware resources, many of the optimizing transformations which proved effective for the PA-8000 served to au ...
- 2** On the instruction-level characteristics of scalar code in highly- 99%

vectorized scientific applications

Sriram Vajapeyam , Wei-Chung Hsu

ACM SIGMICRO Newsletter , Proceedings of the 25th annual international symposium on Microarchitecture December 1992

Volume 23 Issue 1-2
- 3** Beyond induction variables 99%

Michael Wolfe

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1992 conference on Programming language design and implementation July 1992

Volume 27 Issue 7

Induction variable detection is usually closely tied to the strength reduction optimization. This paper studies induction variable analysis from a different perspective, that of finding induction variables for data dependence analysis. While classical induction variable analysis techniques have been used successfully up to now, we have found a simple algorithm based on the Static Single Assignment form

of a program that finds all linear induction variables in a loop. Moreover, this algorithm ...

- 4 SIGCOMM 2003 conference workshop reports: Future directions in network architecture: (FDNA-03) 98%



Steven Bauer , Xiaowei Yang

ACM SIGCOMM Computer Communication Review October 2003

Volume 33 Issue 5

- 5 Height reduction of control recurrences for ILP processors 98%



Michael Schlansker , Vinod Kathail , Sadun Anik

Proceedings of the 27th annual international symposium on Microarchitecture

November 1994

The performance of applications executing on processors with instruction level parallelism is often limited by control and data dependences. Performance bottlenecks caused by dependences can frequently be eliminated through transformations which reduce the height of critical paths through the program. While height reduction techniques are not always helpful, their utility can be demonstrated in a broad range of important situations. This paper focuses on the height reduction of co ...

- 6 Graph rewrite systems for program optimization 97%



Uwe Assmann

ACM Transactions on Programming Languages and Systems (TOPLAS) July 2000

Volume 22 Issue 4

Graph rewrite systems can be used to specify and generate program optimizations. For termination of the systems several rule-based criteria are developed, defining exhaustive graph rewrite systems. For nondeterministic systems stratification is introduced which automatically selects single normal forms. To illustrate how far the methodology reaches, parts of the lazy code motion optimization are specified. The resulting graph rewrite system classes can be e ...

- 7 A data locality optimizing algorithm 97%



Michael E. Wolf , Monica S. Lam

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1991 conference on Programming language design and implementation May 1991

Volume 26 Issue 6

- 8 Probabilistic tangent subspace: a unified view 96%



Jianguo Lee , Jingdong Wang , Changshui Zhang , Zhaoqi Bian

Twenty-first international conference on Machine learning July 2004

Tangent Distance (TD) is one classical method for invariant pattern classification. However, conventional TD need pre-obtain tangent vectors, which is difficult except for image objects. This paper extends TD to more general pattern classification tasks. The basic assumption is that tangent vectors can be approximately represented by the pattern variations. We propose three probabilistic subspace models to encode the variations: the linear subspace, nonlinear subspace, and manifold subspace mode ...

- 9 A register file and scheduling model for application specific processor 96%



synthesis

E. Ercanli , C. Papachristou

Proceedings of the 33rd annual conference on Design automation June 1996

10 Removal of invariant statements from nested-loops in a single effective 95%
compiler pass

D. Neel , M. Amirchahy

ACM SIGPLAN Notices , Proceedings of the conference on Programming languages and compilers for parallel and vector machines January 1975
Volume 10 Issue 3

This document presents how one of the most important optimizations that a program may undergo is dealt with by means of attributes [7]. A semantic formalization of the classical method which consists of removing all loop-independent statements from the articulation blocks of a loop is given. The method is equally well applicable to algebraic languages or their intermediate code : in a high level language even very well constructed programs quite often contain in their intermediate code vers ...

11 Vectorization for SIMD architectures with alignment constraints 95%

Alexandre E. Eichenberger , Peng Wu , Kevin O'Brien

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2004 conference on Programming language design and implementation June 2004
Volume 39 Issue 6

When vectorizing for SIMD architectures that are commonly employed by today's multimedia extensions, one of the new challenges that arise is the handling of memory alignment. Prior research has focused primarily on vectorizing loops where all memory references are properly aligned. An important aspect of this problem, namely, how to vectorize misaligned memory references, still remains unaddressed. This paper presents a compilation scheme that systematically vectorizes loops in the presence of mi ...

12 Technical papers: concurrency: Invariant-based specification, 95%
synthesis, and verification of synchronization in concurrent programs

Xianghua Deng , Matthew B. Dwyer , John Hatcliff , Masaaki Mizuno

Proceedings of the 24th International Conference on Software Engineering May 2002

Concurrency is used in modern software systems as a means of addressing performance, availability, and reliability requirements. The collaboration of multiple independently executing components is fundamental to meeting such requirements and such collaboration is realized by synchronizing component execution. Using current technologies developers are faced with a tension between correct synchronization and performance. Developers can be confident when simple forms of synchronization are used, for ...

13 Optimization of array subscript range checks 94%

Jonathan M. Asuru

ACM Letters on Programming Languages and Systems (LOPLAS) June 1992
Volume 1 Issue 2

Compile-time elimination of subscript range checks is performed by some optimizing compilers to reduce the overhead associated with manipulating array data structures. Elimination and propagation, the two methods of subscript range check optimization, are less effective for eliminating global redundancies especially in while-loop structures with nonconstant loop guards. This paper describes a subscript range check optimization procedure that can eliminate more range checks than

File 347:JAPIO Nov 1976-2004/Aug(Updated 041203)
(c) 2004 JPO & JAPIO
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200504
(c) 2005 Thomson Derwent
File 348:EUROPEAN PATENTS 1978-2005/Jan W03
(c) 2005 European Patent Office
File 349:PCT FULLTEXT 1979-2002/UB=20050127,UT=20050120
(c) 2005 WIPO/Univentio

Set	Items	Description
S1	690	AU=(THOMPSON C? OR LITTFIN J?)
S2	6	S1 AND INVARIANT? ?
S3	2	S2 AND IC=G06F

3/5/1 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
(c) 2005 Thomson Derwent. All rts. reserv.

015997807 **Image available**
WPI Acc No: 2004-155657/200415
XRPX Acc No: N04-124564

Computer program data structure invariant property verification method
involves verifying whether runtime value of data structure is consistent
with invariant property during execution

Patent Assignee: LITTFIN J (LITT-I); THOMPSON C L (THOM-I)

Inventor: LITTFIN J ; THOMPSON C L

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20040015897	A1	20040122	US 2001858241	A	20010515	200415 B

Priority Applications (No Type Date): US 2001858241 A 20010515

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20040015897	A1		6	G06F-009/44	

Abstract (Basic): US 20040015897 A1

NOVELTY - A code verifying whether a runtime value of data structure is consistent with **invariant** property, is automatically generated in response to annotation of data structure in source code. The runtime value is compared to **invariant** property in the generated code, during execution of program. If the runtime property is inconsistent with the **invariant** property, the program branches to exception handler code.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for apparatus for verifying **invariant** property of data structure of computer program at run time.

USE - For verifying **invariant** property which is range of data values, of data structures of computer programs in variety of programming languages for e.g. C and C++, at runtime.

ADVANTAGE - Avoids the need for a developer to specify the range of valid addresses for code or data, since the compiler determines the valid range of addresses of code or data. Realizes a method capable of being applicable to a variety of programming languages particularly beneficial in compiled languages, for e.g. C and C++.

DESCRIPTION OF DRAWING(S) - The figure shows the flowchart explaining the process of verifying **invariant** properties of data structures at runtime.

pp; 6 DwgNo 2/2

Title Terms: COMPUTER; PROGRAM; DATA; STRUCTURE; **INVARIANT** ; PROPERTIES; VERIFICATION; METHOD; VERIFICATION; VALUE; DATA; STRUCTURE; CONSISTENT; **INVARIANT** ; PROPERTIES; EXECUTE

Derwent Class: T01

International Patent Class (Main): G06F-009/44

International Patent Class (Additional): G06F-009/45 ; H02H-003/05

File Segment: EPI

3/5/2 (Item 1 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00483182

Apparatus and method for adding an associative query capability to a programming language

Verfahren und Gerät um eine inhaltsadressierbare Abfragemöglichkeit einer Programmiersprache hinzuzufügen

Procede et dispositif pour ajouter la possibilite d'une interrogation associative a un langage de programmation

PATENT ASSIGNEE:

TEXAS INSTRUMENTS INCORPORATED, (279070), 13500 North Central Expressway, Dallas Texas 75265, (US), (applicant designated states: DE;FR;GB;IT;NL)

INVENTOR:

Blakely, Jose A., 4105 Norcross Drive, Plano, Texas 75024, (US)

Thompson, Craig W. , 2725 Deep Valley Trail, Plano, Texas 75023, (US)

LEGAL REPRESENTATIVE:

Abbott, David John et al (27491), Abel & Imray 20 Red Lion Street, London
WC1R 4PQ, (GB)

PATENT (CC, No, Kind, Date): EP 455447 A2 911106 (Basic)
EP 455447 A3 930630
EP 455447 B1 990616

APPLICATION (CC, No, Date): EP 91303851 910429;

PRIORITY (CC, No, Date): US 516369 900430

DESIGNATED STATES: DE; FR; GB; IT; NL

INTERNATIONAL PATENT CLASS: **G06F-017/60**

CITED REFERENCES (EP A):

ANGEWANDTE INFORMATIK. APPLIED INFORMATICS vol. 29, no. 4, April 1987,

WIESBADEN DE pages 149 - 156 J. NOACK & H. KUCHEN : 'MEMOPASCAL:

Einbettung einer Datenbankanfragesprache in eine h here
Programmiersprache'

PROCEEDINGS OF THE 15TH INTERNATIONAL CONFERENCE ON VERY LARGE DATA BASES

22 August 1989, AMSTERDAM, NL pages 433 - 442 A. ALASHQUR ET AL : 'OQL:
a query language for manipulating object-oriented databases'

22ND ACM/IEEE DESIGN AUTOMATION CONFERENCE 23 June 1985, LAS VEGAS, US

pages 577 - 583 G. BARABINO ET AL : 'A module for improving data access
and management in an integrated CAD environment';

ABSTRACT EP 455447 A2

An object-oriented query language apparatus and method provides data abstraction, seamlessness with respect to the host programming language, set support orthogonal to persistence and strong typing. Such apparatus for processing queries includes a preprocessor (144,148) to parse, optimize, and translate object query language statements into efficient host language statements, connected to a compiler (152) that compiles such host language statements into executable code (154). Therefore, by inputting a program containing statements using a host language's type system as well as employing the host language's expressions, object composition, and inheritance in the formulation of queries, and by combining the Select-From-Where style of Standard Query Language as a basic structure for query statements with the set-valued function of the host language as targets for those queries, an associative query capability is added to a host programming language, thereby improving its versatility and functionality. (see image in original document)

File 8: Ei Compendex(R) 1970-2005/Jan W3
(c) 2005 Elsevier Eng. Info. Inc.
File 35: Dissertation Abs Online 1861-2004/Dec
(c) 2004 ProQuest Info&Learning
File 65: Inside Conferences 1993-2005/Jan W4
(c) 2005 BLDSC all rts. reserv.
File 2: INSPEC 1969-2005/Jan W3
(c) 2005 Institution of Electrical Engineers
File 94: JICST-EPlus 1985-2005/Dec W3
(c) 2005 Japan Science and Tech Corp(JST)
File 483: Newspaper Abs Daily 1986-2005/Jan 27
(c) 2005 ProQuest Info&Learning
File 6: NTIS 1964-2005/Jan W3
(c) 2005 NTIS, Intl Cpyrght All Rights Res
File 144: Pascal 1973-2005/Jan W3
(c) 2005 INIST/CNRS
File 434: SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info
File 34: SciSearch(R) Cited Ref Sci 1990-2005/Jan W4
(c) 2005 Inst for Sci Info
File 99: Wilson Appl. Sci & Tech Abs 1983-2004/Nov
(c) 2004 The HW Wilson Co.
File 583: Gale Group Globalbase(TM) 1986-2002/Dec 13
(c) 2002 The Gale Group
File 266: FEDRIP 2004/Oct
Comp & dist by NTIS, Intl Copyright All Rights Res
File 95: TEME-Technology & Management 1989-2004/Jun W1
(c) 2004 FIZ TECHNIK
File 438: Library Lit. & Info. Science 1984-2004/Oct
(c) 2004 The HW Wilson Co

Set	Items	Description
S1	235048	INVARIANT? ?
S2	3755	RANGE? ?(7N) (ADDRESS OR ADDRESSES)
S3	19319	(SERIES OR SEQUENCE? ? OR SUCCESSION OR SUCCESSIVE OR CONSECUTIVE OR ARRAY? ? OR GROUP???? OR CLUSTER? ? OR SET? ? OR COLLECTION? ? OR FAMILY OR FAMILIES? ?) (7N) (ADDRESS OR ADDRESSES)
S4	16	S1(10N)S2:S3
S5	12	RD (unique items)
S6	12344	AU=(THOMPSON, C? OR LITTFIN, J? OR THOMPSON C? OR LITTFIN - J?)
S7	30	S1 AND S6
S8	17	RD (unique items)

5/5/7 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

7624761 INSPEC Abstract Number: B2003-06-6120B-066

Title: Permutation group of some concatenated block codes

Author(s): Lacan, J.

Author Affiliation: ENSICA, Toulouse, France

Conference Title: Proceedings 2002 IEEE International Symposium on Information Theory (Cat. No.02CH37371) p.33

Publisher: IEEE, Piscataway, NJ, USA

Publication Date: 2002 Country of Publication: USA xxxiii+507 pp.

ISBN: 0 7803 7501 7 Material Identity Number: XX-2002-00202

U.S. Copyright Clearance Center Code: 0-7803-7501-7/02/\$17.00

Conference Title: Proceedings of IEEE International Symposium on Information Theory

Conference Sponsor: IEEE Inf. Theory Soc

Conference Date: 30 June-5 July 2002 Conference Location: Lausanne, Switzerland

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: An important area in coding theory deals with codes which are invariant under permutations. In some cases, the permutations can be used to obtain new results on the minimum distance or on decoding algorithms. In this work, we **address** the problem of constructing **families** of concatenated codes which are **invariant** under the action of some permutations. (2 Refs)

Subfile: B

Descriptors: block codes; concatenated codes; group codes

Identifiers: concatenated block codes; permutation group; coding theory; minimum distance; decoding algorithms

Class Codes: B6120B (Codes)

Copyright 2003, IEE

5/5/9 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

5584319 INSPEC Abstract Number: C9706-4210L-061

Title: Type checking for a multiset rewriting language

Author(s): Fradet, P.; Le Metayer, D.

Author Affiliation: IRISA, Rennes, France

Conference Title: Analysis and Verification of Multiple-Agent Languages. 5th LOMAPS Workshop. Selected Papers p.138-40

Editor(s): Dam, M.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1997 Country of Publication: Germany vii+434 pp.

ISBN: 3 540 62503 8 Material Identity Number: XX97-00233

Conference Title: Analysis and Verification of Multiple-Agent Languages

Conference Date: 24-26 June 1996 Conference Location: Stockholm, Sweden

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: The authors enhance Gamma, a multiset rewriting language, with a notion of structured multiset. A structured multiset is a set of addresses satisfying specific relations which can be used in the rewriting rules of the program. A type is defined by a context-free graph grammar and a structured multiset belongs to a type T if its underlying **set** of **addresses** satisfies the **invariant** expressed by the grammar defining T. They define a type checking algorithm which allows one to prove mechanically that a program maintains its data structure invariant. (13 Refs)

Subfile: C

Descriptors: data structures; graph grammars; program verification; rewriting systems; type theory

Identifiers: multiset rewriting language; Gamma; type checking; structured multiset; addresses; relations; program rewriting rules;

context-free graph grammar; type checking algorithm; data structure
Class Codes: C4210L (Formal languages and computational linguistics);
C6110F (Formal methods); C4240 (Programming and algorithm theory); C6120
(File organisation)
Copyright 1997, IEE

5/5/11 (Item 5 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

02173493 INSPEC Abstract Number: C84005714

Title: An interactive approach to software specification

Author(s): Smoliar, S.W.

Author Affiliation: Schlumberger-Doll Res., Ridgefield, CT, USA

Conference Title: Integrated Interactive Computing Systems. Proceedings
of the European Conference, ECICS 82 p.223-33

Editor(s): Degano, P.; Sandewall, E.

Publisher: North-Holland, Amsterdam, Netherlands

Publication Date: 1983 Country of Publication: Netherlands xi+374 pp.

ISBN: 0 444 86595 0

Conference Sponsor: ACM; AFCET; AICA; et al

Conference Date: 1-3 Sept. 1982 Conference Location: Stresa, Italy

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: This is a report of a preliminary version of an interactive system for the preparation of formal software specifications. The specifications in question are concerned with the development of numerical software for scientific applications. As such, they must **address** the representation of a **collection** of mathematical **invariants** and some statement as to how these **invariants** are to be used to achieve a given computational task. The approach involves the development of a system in which a formal specification may be derived from informal specifications provided through a dialog with a user whose primary concern is his scientific application, rather than the details of programming. To achieve this goal, a specification system has been built using the constructs of an object-oriented, frame-based knowledge-representation language. (24 Refs)

8/TI/1 (Item 1 from file: 8)

DIALOG(R)File 8:(c) 2005 Elsevier Eng. Info. Inc. All rts. reserv.

Title: Linear stochastic dynamical model of ENSO. Part I: model development

8/TI/2 (Item 2 from file: 8)

DIALOG(R)File 8:(c) 2005 Elsevier Eng. Info. Inc. All rts. reserv.

Title: Diffusive media characterization with laser speckle

8/TI/3 (Item 1 from file: 35)

DIALOG(R)File 35:(c) 2004 ProQuest Info&Learning. All rts. reserv.

A LINEAR, STOCHASTIC, DYNAMICAL MODEL OF EL NINO/SOUTHERN OSCILLATION
(OCEAN-ATMOSPHERE, EXTERNAL FORCING)

8/TI/4 (Item 1 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: Analytic model for the development of bamboo microstructures in thin film strips undergoing normal grain growth

8/TI/5 (Item 2 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: Steady-state grain-size distributions resulting from grain growth in two dimensions

8/TI/6 (Item 3 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: Baryogenesis in a magnetized universe

8/TI/7 (Item 4 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: Initial conditions for optimal growth in a coupled ocean-atmosphere model of ENSO

8/TI/8 (Item 5 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: On the nature of multiple orientation relationships in epitaxial silicides

8/TI/9 (Item 6 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts. reserv.

Title: Iteration of piecewise linear maps on an interval

8/TI/10 (Item 7 from file: 2)

DIALOG(R)File 2:(c) 2005 Institution of Electrical Engineers. All rts.

•
reserv.

Title: Phase transition in a non-translationally invariant spherical model

8/TI/11 (Item 1 from file: 6)
DIALOG(R)File 6:(c) 2005 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

CD28 is an Inducible T Cell Surface Antigen that Transduces a Proliferative Signal in CD3(+) Mature Thymocytes
(Journal article)

8/TI/12 (Item 2 from file: 6)
DIALOG(R)File 6:(c) 2005 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

Levels of c-myc Oncogene mRNA are Invariant Throughout the Cell Cycle
(Final rept. 1984-1985)

8/TI/13 (Item 1 from file: 144)
DIALOG(R)File 144:(c) 2005 INIST/CNRS. All rts. reserv.

Effects of 3-D complexity on the perception of 2-D depictions of objects

8/TI/14 (Item 2 from file: 144)
DIALOG(R)File 144:(c) 2005 INIST/CNRS. All rts. reserv.

DISTRIBUTION OF ITERATES OF FIRST ORDER DIFFERENCE EQUATIONS

8/TI/15 (Item 1 from file: 34)
DIALOG(R)File 34:(c) 2005 Inst for Sci Info. All rts. reserv.

Title: B7/CD28-DEPENDENT AND B7/CD28-INDEPENDENT INDUCTION OF CD40 LIGAND EXPRESSION

8/TI/16 (Item 2 from file: 34)
DIALOG(R)File 34:(c) 2005 Inst for Sci Info. All rts. reserv.

Title: CD43 IS A MURINE T-CELL COSTIMULATORY RECEPTOR THAT FUNCTIONS INDEPENDENTLY OF CD28

8/TI/17 (Item 3 from file: 34)
DIALOG(R)File 34:(c) 2005 Inst for Sci Info. All rts. reserv.

Title: SUBSONIC POTENTIAL FLOW AND THE TRANSONIC CONTROVERSY

File 275:Gale Group Computer DB(TM) 1983-2005/Jan 28
(c) 2005 The Gale Group
File 621:Gale Group New Prod.Annou.(R) 1985-2005/Jan 28
(c) 2005 The Gale Group
File 636:Gale Group Newsletter DB(TM) 1987-2005/Jan 28
(c) 2005 The Gale Group
File 16:Gale Group PROMT(R) 1990-2005/Jan 28
(c) 2005 The Gale Group
File 160:Gale Group PROMT(R) 1972-1989
(c) 1999 The Gale Group
File 148:Gale Group Trade & Industry DB 1976-2005/Jan 27
(c)2005 The Gale Group
File 624:McGraw-Hill Publications 1985-2005/Jan 27
(c) 2005 McGraw-Hill Co. Inc
File 15:ABI/Inform(R) 1971-2005/Jan 27
(c) 2005 ProQuest Info&Learning
File 647:CMP Computer Fulltext 1988-2005/Jan W2
(c) 2005 CMP Media, LLC
File 674:Computer News Fulltext 1989-2005/Jan W3
(c) 2005 IDG Communications
File 696:DIALOG Telecom. Newsletters 1995-2005/Jan 27
(c) 2005 The Dialog Corp.
File 369:New Scientist 1994-2005/Jan W3
(c) 2005 Reed Business Information Ltd.

Set	Items	Description
S1	5414	INVARIANT? ?
S2	25396	RANGE? ?(7N) (ADDRESS OR ADDRESSES)
S3	92466	(SERIES OR SEQUENCE? ? OR SUCCESSION OR SUCCESSIVE OR CONS- ECUTIVE OR ARRAY? ? OR GROUP???? OR CLUSTER? ? OR SET? ? OR C- OLLECTION? ? OR FAMILY OR FAMILIES? ?) (7N) (ADDRESS OR ADDRESS- ES)
S4	0	S1(10N)S2:S3
S5	2	S1(20N)S2:S3
S6	2	RD (unique items)

6/3,K/1 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2005 The Gale Group. All rts. reserv.

10161506 SUPPLIER NUMBER: 20117363 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Portfolio mix and large-bank profitability in the USA.

Miller, Stephen M.; Noulas, Athanasios G.

Applied Economics, v29, n4, p505(8)

April, 1997

ISSN: 0003-6846 LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 4402 LINE COUNT: 00354

... movements that may be in the data.

The pooled cross-section, time- **series** regressions allow us to **address** some problems with unobservable data. We can conceptually divide the pooled data into three different groups: time- **invariant** and bank-varying variables (e.g. bank location); bank-invariant and time...

6/3,K/2 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2005 The Gale Group. All rts. reserv.

07584864 SUPPLIER NUMBER: 16467482 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Portfolio mix and net charge offs at large United States commercial banks.

(Applied Economics Letters)

Miller, Stephen M.; Noulas, Athanasios G.

Applied Economics, v26, n11, pS183(4)

Nov, 1994

ISSN: 0003-6846 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

WORD COUNT: 2393 LINE COUNT: 00189

... salaries and benefits to total employees).

We perform pooled cross-section time- **series** regressions, which allows us to **address** some problems with unobservable data. We can conceptually divide the pooled data into three different groups -- time- **invariant** and bank-varying variables (e.g. bank location), bank-invariant and time...

?

File 347:JAPIO Nov 1976-2004/Aug(Updated 041203)

(c) 2004 JPO & JAPIO

File 350:Derwent WPIX 1963-2005/UD,UM &UP=200504

(c) 2005 Thomson Derwent

Set	Items	Description
S1	1706	INVARIANT? ?
S2	2903	RANGE? ?(7N) (ADDRESS OR ADDRESSES)
S3	34585	(SERIES OR SEQUENCE? ? OR SUCCESSION OR SUCCESSIVE OR CONSECUTIVE OR ARRAY? ? OR GROUP???? OR CLUSTER? ? OR SET? ? OR COLLECTION? ? OR FAMILY OR FAMILIES? ?) (7N) (ADDRESS OR ADDRESSES)
S4	0	S1(10N)S2:S3
S5	4	S1 AND S2:S3

5/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2005 Thomson Derwent. All rts. reserv.

015997807 **Image available**
WPI Acc No: 2004-155657/200415
XRPX Acc No: N04-124564

Computer program data structure invariant property verification method
involves verifying whether runtime value of data structure is consistent
with invariant property during execution

Patent Assignee: LITTFIN J (LITT-I); THOMPSON C L (THOM-I)

Inventor: LITTFIN J; THOMPSON C L

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20040015897	A1	20040122	US 2001858241	A	20010515	200415 B

Priority Applications (No Type Date): US 2001858241 A 20010515

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20040015897	A1		6	G06F-009/44	

Abstract (Basic): US 20040015897 A1

NOVELTY - A code verifying whether a runtime value of data structure is consistent with **invariant** property, is automatically generated in response to annotation of data structure in source code. The runtime value is compared to **invariant** property in the generated code, during execution of program. If the runtime property is inconsistent with the **invariant** property, the program branches to exception handler code.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for apparatus for verifying **invariant** property of data structure of computer program at run time.

USE - For verifying **invariant** property which is range of data values, of data structures of computer programs in variety of programming languages for e.g. C and C++, at runtime.

ADVANTAGE - Avoids the need for a developer to specify the **range** of valid **addresses** for code or data, since the compiler determines the valid **range** of **addresses** of code or data. Realizes a method capable of being applicable to a variety of programming languages particularly beneficial in compiled languages, for e.g. C and C++.

DESCRIPTION OF DRAWING(S) - The figure shows the flowchart explaining the process of verifying **invariant** properties of data structures at runtime.

pp; 6 DwgNo 2/2

Title Terms: COMPUTER; PROGRAM; DATA; STRUCTURE; **INVARIANT** ; PROPERTIES; VERIFICATION; METHOD; VERIFICATION; VALUE; DATA; STRUCTURE; CONSISTENT; **INVARIANT** ; PROPERTIES; EXECUTE

Derwent Class: T01

International Patent Class (Main): G06F-009/44

International Patent Class (Additional): G06F-009/45; H02H-003/05

File Segment: EPI

5/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2005 Thomson Derwent. All rts. reserv.

014885454 **Image available**
WPI Acc No: 2002-706160/200276
Related WPI Acc No: 2002-706196
XRPX Acc No: N02-556741

Computer readable medium storing traffic demand measuring program for internet, involves aggregating network traffic between ingress and egress links.

Patent Assignee: FELDMANN A (FELD-I); GREENBERG A G (GREE-I); LUND C

(LUND-I); REINGOLD N (REIN-I); REXFORD J L (REXF-I); TRUE F D (TRUE-I)

Inventor: FELDMANN A; GREENBERG A G; LUND C; REINGOLD N; REXFORD J L; TRUE

F D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020101821	A1	20020801	US 2000199091	A	20000421	200276 B
			US 2000661517	A	20000913	
			US 2001876383	A	20010607	

Priority Applications (No Type Date): US 2000199091 P 20000421; US

2000661517 A 20000913; US 2001876383 A 20010607

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20020101821	A1		17	H04L-001/00	Provisional application US 2000199091

Cont of application US 2000661517

Abstract (Basic): US 20020101821 A1

NOVELTY - The destination network **address** is associated with a **set** of egress links. The network traffic demand is computed by aggregating the network traffic traveling between an ingress and one egress link.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is included for packet switched network's traffic demand measurement method.

USE - For measuring the traffic demand in packet switched network such as internet.

ADVANTAGE - Effectively handles interdomain traffic and is **invariant** to changes in the internal routing configuration, as the traffic flow between the ingress and egress links is aggregated.

DESCRIPTION OF DRAWING(S) - The figure shows an explanatory view of internet protocol backbone network.

pp; 17 DwgNo 2/7

Title Terms: COMPUTER; READ; MEDIUM; STORAGE; TRAFFIC; DEMAND; MEASURE;

PROGRAM; AGGREGATE; NETWORK; TRAFFIC; INGRESS; EGRESS; LINK

Derwent Class: T01; W01

International Patent Class (Main): H04L-001/00

International Patent Class (Additional): H04J-001/16

File Segment: EPI

5/5/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2005 Thomson Derwent. All rts. reserv.

013499923 **Image available**

WPI Acc No: 2000-671864/200065

XRPX Acc No: N00-498031

Computer-implemented information locating method involves searching for particular set of data for applicable information, if particular address is determined to be included in particular address range

Patent Assignee: SUN MICROSYSTEMS INC (SUNM)

Inventor: DELAGI B A; MILLER T C; TATKAR V K; ZUCKER J S

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6119206	A	20000912	US 9611522	A	19960212	200065 B
			US 96680575	A	19960712	

Priority Applications (No Type Date): US 9611522 P 19960212; US 96680575 A 19960712

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 6119206	A		19	G06F-012/06	Provisional application US 9611522

Abstract (Basic): US 6119206 A

NOVELTY - The method starts with locating a particular set of data that applies to a particular object, which may either be a primary or shared object. A particular **address range** to which the particular **set** of data applies is determined. If the particular **address range**

is determined to include a particular **address** , the particular **set** of data for the information that applies to the particular address is searched.

DETAILED DESCRIPTION - Each object is loaded into an address space during execution of the program, includes a **set** of compiled instructions and occupies a respective **address range** . INDEPENDENT CLAIMS are also included for the following;

- (a) a computer program product;
- (b) and a method for retrieving values of non-volatile registers stored in a stack frame.

USE - For accessing main memory.

ADVANTAGE - Locates various types of information that applies to particular address in program executing in environment supporting shared objects. Utilizes offset fields in tags to permit tag's contents to remain **invariant** regardless of location in program's address space at which object containing tags is loaded. Improves program's computational efficiency by eliminating need to save, in stack frame, non-volatile registers that have not changed since function entry.

DESCRIPTION OF DRAWING(S) - The figure shows the flowchart for performing a stack trace back using tags.

pp; 19 DwgNo 3/6

Title Terms: COMPUTER; IMPLEMENT; INFORMATION; LOCATE; METHOD; SEARCH; SET;

DATA; APPLY; INFORMATION; ADDRESS; DETERMINE; ADDRESS; RANGE

Derwent Class: T01

International Patent Class (Main): G06F-012/06

File Segment: EPI

5/5/4 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2005 Thomson Derwent. All rts. reserv.

013333127 **Image available**

WPI Acc No: 2000-505066/200045

XRFX Acc No: N00-373434

Integrated circuit with built-in self-test and self-repair for memory device, generates signature based on compression of addresses corresponding to fault memory locations

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: BRUCE W C; GILES G L; KANBE K K

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6085334	A	20000704	US 9861983	A	19980417	200045 B

Priority Applications (No Type Date): US 9861983 A 19980417

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6085334	A	10	H02H-003/05	

Abstract (Basic): US 6085334 A

NOVELTY - A built-in self-test circuit (30) provides **address** of fault memory locations in memory **array** (40). A built-in self-repair circuit (39) replaces failed memory locations to spare memory element. A built-in self-monitor (31) generates signature based on compression of addresses corresponding to the locations to determine that repair result of array is **invariant** over different environmental conditions.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for method of testing and repairing embedded memory.

USE - For built-in self-test and self-repair of memory devices e.g. SRAM, DRAM, CAM.

ADVANTAGE - Detects memory device defects which are sensitive to environmental conditions and are not exhibited over the entire operating range of the device. Risk of aliasing is reduced by the generation of signature.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of data processing system.

Self-test circuit (30)

File 348:EUROPEAN PATENTS 1978-2005/Jan W03

(c) 2005 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20050127,UT=20050120

(c) 2005 WIPO/Univentio

Set	Items	Description
S1	10072	INVARIANT? ?
S2	6507	RANGE? ?(7N) (ADDRESS OR ADDRESSES)
S3	44614	(SERIES OR SEQUENCE? ? OR SUCCESSION OR SUCCESSIVE OR CONSECUTIVE OR ARRAY? ? OR GROUP???? OR CLUSTER? ? OR SET? ? OR COLLECTION? ? OR FAMILY OR FAMILIES? ?) (7N) (ADDRESS OR ADDRESSES)
S4	16	S1(10N)S2:S3

4/3,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

01426644

Method and apparatus for implementing anti-counterfeiting measures in personal computer-based digital color printers
Verfahren und Gerat um Antifalschungsmassnahmen in auf PC basierten digitaler Farbdrucker anzuwenden

Methode et dispositif d'implementation des mesures contre la contrefaçon dans des imprimantes couleur numeriques a base d'ordinateur personnel
PATENT ASSIGNEE:

Xerox Corporation, (219788), Xerox Square - 20A, 100 Clinton Avenue South, Rochester, New York 14644, (US), (Applicant designated States: all)

INVENTOR:

Zhigang, Fan, 153 Yorktown Drive, Webster, NY 14580, (US)

LEGAL REPRESENTATIVE:

Grunecker, Kinkeldey, Stockmair & Schwanhauser Anwaltssozietat (100721), Maximilianstrasse 58, 80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1204020 A2 020508 (Basic)

APPLICATION (CC, No, Date): EP 2001126045 011031;

PRIORITY (CC, No, Date): US 707409 001106

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-003/12

ABSTRACT WORD COUNT: 100

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200219	427
SPEC A	(English)	200219	4645
Total word count - document A			5072
Total word count - document B			0
Total word count - documents A + B			5072

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses**. ROM 122 stores font data, program instruction **sequences** used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

4/3,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

01417805

Improved print head recovery

Verbesserte Reinigungsverfahren eines Druckkopfes

Remise en etat amelioree d'une tete d'impression

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku, Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Onuma, Kentaro, c/o Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Hanabusa, Tadashi, c/o Canon Business Machines Inc, 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Nitta, Tetsuhiro, c/o Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, c/o Canon Business, Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Kanemitsu, Shinji, c/o Canon Business Machines Inc, 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick Court, High Holborn, London WC1R 5DH, (GB)
PATENT (CC, No, Kind, Date): EP 1197337 A2 020417 (Basic)
EP 1197337 A3 020612
APPLICATION (CC, No, Date): EP 2001307767 010912;
PRIORITY (CC, No, Date): US 661388 000913
DESIGNATED STATES: DE; FR; GB; IT
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS: B41J-002/165
ABSTRACT WORD COUNT: 136
NOTE:

Figure number on first page: 14

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200216	5415
SPEC A	(English)	200216	17174
Total word count - document A			22589
Total word count - document B			0
Total word count - documents A + B			22589

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 92 stores font data, program instruction **sequences** used to control printer 10, and other **invariant** data for printer operation. RAM 99 stores print data in a print...

4/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

01406190

Printer with fast line feed speed

Drucker mit hoher Zeilenvorschubgeschwindigkeit

Imprimante avec avancement d'interligne a haute vitesse

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku, Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Hanabusa, Tadashi, c/o Canon Business Machines Inc, 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Yamada, Akitoshi, c/o Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, c/o Canon Business, Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick Court, High Holborn, London WC1R 5DH, (GB)

PATENT (CC, No, Kind, Date): EP 1188569 A2 020320 (Basic)
EP 1188569 A3 030806

APPLICATION (CC, No, Date): EP 2001307766 010912;

PRIORITY (CC, No, Date): US 661034 000913

DESIGNATED STATES: DE; FR; GB; IT

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: B41J-019/78; B41J-002/505; G06K-015/10;
H04N-001/17; H04N-001/40

ABSTRACT WORD COUNT: 230

NOTE:

Figure number on first page: 9

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200212	3634
SPEC A	(English)	200212	13795
Total word count - document A			17429
Total word count - document B			0

Total word count - documents A + B 17429

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 92 stores font data, program instruction **sequences** used to control printer 10, and other **invariant** data for printer operation. RAM 99 stores print data in a print...

4/3,K/4 (Item 4 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01403406

Printer with improved page feed

Drucker mit verbesserter Seitenzufuhr

Imprimante a alimentation de pages amelioree

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku, Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Hamamoto, Akihiko, Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Yamada, Akitoshi, Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Cheng, Peter L., Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Aichi, Takao, Canon Business Machines Inc., 3191 Red Hill Avenue, Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, Canon Business Machines Inc., 3191 Red Hill Ave., Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28275), Beresford & Co., 2-5 Warwick Court, High Holborn, London WC1R 5DH, (GB)

PATENT (CC, No, Kind, Date): EP 1186430 A2 020313 (Basic)

EP 1186430 A3 021120

APPLICATION (CC, No, Date): EP 2001307707 010911;

PRIORITY (CC, No, Date): US 660352 000912

DESIGNATED STATES: DE; FR; GB; IT

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: B41J-013/00

ABSTRACT WORD COUNT: 132

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
----------------	----------	--------	------------

CLAIMS A	(English)	200211	1348
----------	-----------	--------	------

SPEC A	(English)	200211	13144
--------	-----------	--------	-------

Total word count - document A	14492
-------------------------------	-------

Total word count - document B	0
-------------------------------	---

Total word count - documents A + B	14492
------------------------------------	-------

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 92 stores font data, program instruction **sequences** used to control printer 10, and other **invariant** data for printer operation. RAM 99 stores print data in a print...

4/3,K/5 (Item 5 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01270238

High resolution printing

Drucken mit hoher Auflosung

Impression a haute resolution

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,
Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Yamada, Akitoshi, 3191 Red Hill Avenue, Costa Mesa, California 92626,
(US)

Hirabayashi, Hiromitsu, 3191 Red Hill Avenue, Costa Mesa, California
92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28274), 2-5 Warwick Court High
Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 1093923 A1 010425 (Basic)

APPLICATION (CC, No, Date): EP 308952 001011;

PRIORITY (CC, No, Date): US 422111 991020

DESIGNATED STATES: DE; FR; GB; IT

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: B41J-002/21; B41J-002/505; G06K-015/10;
B41J-019/14

ABSTRACT WORD COUNT: 119

NOTE:

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200117	3120
SPEC A	(English)	200117	13017
Total word count - document A			16137
Total word count - document B			0
Total word count - documents A + B			16137

...SPECIFICATION exchange of control signals and to receive print data and
print data **addresses** . ROM 122 stores font data, program instruction
sequences used to control printer 30, and other **invariant** data for
printer operation. RAM 129 stores print data in a print...

4/3,K/6 (Item 6 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01029635

A printer having a memory for storing a printer profile parameter

Ein Drucker mit einem Speicher zur Speicherung eines
Druckerprofilparameters

Une imprimante avec une memoire pour memoriser un parametre d'un profil
d'imprimante

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,
Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Kawanabe, Tetsuya, Canon Business Machines, Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

Sukigara, Akihiko, Canon Business Machines, Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

Masumoto, Kazuyuki, Canon Business Machines, Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, Canon Business Machines, Inc., 3191 Red Hill Ave.
, Costa Mesa, California 92626, (US)

Yamada, Akitoshi, Canon Business Machines, Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

Aichi, Takao, c/o Canon Business Machines, Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick
Court, High Holborn, London WC1R 5DH, (GB)

PATENT (CC, No, Kind, Date): EP 917096 A2 990519 (Basic)

EP 917096 A3 021106

APPLICATION (CC, No, Date): EP 98309344 981113;

PRIORITY (CC, No, Date): US 972309 971117
DESIGNATED STATES: DE; ES; FR; GB; IT; NL
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS: G06K-015/10; G06K-015/00; G06F-003/12
ABSTRACT WORD COUNT: 92
NOTE:

Figure number on first page: 14

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9920	1632
SPEC A	(English)	9920	52044
Total word count - document A			53676
Total word count - document B			0
Total word count - documents A + B			53676

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 122 stores font data, program instruction **sequences** used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

4/3,K/7 (Item 7 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

01029620

Control over print head driving parameters
Steuerung der Treibparameter eines Druckkopfes
Commande des parametres d'entrainement d'une tete d'impression
PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,
Tokyo, (JP), (Proprietor designated states: all)

INVENTOR:

Kamada, Masashi, Canon Business Mach., Inc., 3191 Red Hill Avenue, Costa
Mesa, California 92626, (US)
Sukigara, Akihiko, Canon Business Mach., Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)
Hirabayashi, Hiromitsu, Canon Business Mach., Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)
Yamada, Akitoshi, Canon Business Mach., Inc., 3191 Red Hill Avenue, Costa
Mesa, California 92626, (US)
Masumoto, Kazuyuki, Canon Business Mach., Inc., 3191 Red Hill Avenue,
Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 16 High
Holborn, London WC1V 6BX, (GB)

PATENT (CC, No, Kind, Date): EP 916495 A2 990519 (Basic)
EP 916495 A3 000628
EP 916495 B1 040303

APPLICATION (CC, No, Date): EP 98309316 981113;
PRIORITY (CC, No, Date): US 972307 971117
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS: B41J-002/05
ABSTRACT WORD COUNT: 129
NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199920	2743
CLAIMS B	(English)	200410	1080
CLAIMS B	(German)	200410	1033
CLAIMS B	(French)	200410	1161

SPEC A	(English)	199920	51783
SPEC B	(English)	200410	51757
Total word count - document A			54535
Total word count - document B			55031
Total word count - documents A + B			109566

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 122 stores font data, program instruction **sequences** used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 122 stores font data, program instruction **sequences** used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

4/3,K/8 (Item 8 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
 (c) 2005 European Patent Office. All rts. reserv.

01029605

Multi-head printing with differing resolutions

Drucken mit mehreren Kopfen mit verschiedenen Auflösungen

Impression a plusieurs tetes a differentes resolutions

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542362), 30-2, Shimomaruko 3-chome, Ohta-ku
 Tokyo 146-8501, (JP), (Proprietor designated states: all)

INVENTOR:

Yamada, Akitoshi, Canon Business Machines, Inc., 3191 Red Hill Avenue,
 Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, Canon Business Machines, Inc., 3191 Red Hill Ave.,
 Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 16 High
 Holborn, London WC1V 6BX, (GB)

PATENT (CC, No, Kind, Date): EP 916494 A2 990519 (Basic)
 EP 916494 A3 000705
 EP 916494 B1 040303

APPLICATION (CC, No, Date): EP 98309285 981113;

PRIORITY (CC, No, Date): US 972113 971117

DESIGNATED STATES: DE; ES; FR; GB; IT; NL

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: B41J-002/05; B41J-029/00; B41J-002/505

ABSTRACT WORD COUNT: 110

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199920	2699
CLAIMS B	(English)	200410	1451
CLAIMS B	(German)	200410	1300
CLAIMS B	(French)	200410	1523
SPEC A	(English)	199920	51740
SPEC B	(English)	200410	51724
Total word count - document A			54448
Total word count - document B			55998
Total word count - documents A + B			110446

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 122 stores font data, program instruction **sequences** used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

...SPECIFICATION exchange of control signals and to receive print data and print data **addresses** . ROM 122 stores font data, program instruction

sequences used to control printer 30, and other **invariant** data for printer operation. RAM 129 stores print data in a print...

4/3,K/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01029604

Serial printer with addressable print buffer

Seriendrucker mit adressierbarem Druckpuffer

Imprimante serie a tampon d'impression adressable

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,
Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Masumoto, Kazuyuki, Canon Business Machines, Inc., 3191 Red Hill Ave.,
Costa Mesa, California 92626, (US)

Hirabayashi, Hiromitsu, Canon Business Machines, Inc., 3191 Red Hill Ave.,
Costa Mesa, California 92626, (US)

Yamada, Akitoshi, Canon Business Machines, Inc., 3191 Red Hill Ave.,
Costa Mesa, California 92626, (US)

Aichi, Takao, Canon Business Machines, Inc., 3191 Red Hill Ave., Costa
Mesa, California 92626, (US)

Sukigara, Akihiko, Canon Business Machines, Inc., 3191 Red Hill Ave.,
Costa Mesa, California 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. High Holborn
2-5 Warwick Court, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 917095 A2 990519 (Basic)
EP 917095 A3 000112

APPLICATION (CC, No, Date): EP 98309284 981113;

PRIORITY (CC, No, Date): US 972140 971117

DESIGNATED STATES: DE; ES; FR; GB; IT; NL

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06K-015/10

ABSTRACT WORD COUNT: 102

NOTE:

Figure number on first page: 43-3

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9920	3317
SPEC A	(English)	9920	51932
Total word count - document A			55249
Total word count - document B			0
Total word count - documents A + B			55249

...SPECIFICATION exchange of control signals and to receive print data and
print data **addresses** . ROM 122 stores font data, program instruction
sequences used to control printer 30, and other **invariant** data for
printer operation. RAM 129 stores print data in a print...

4/3,K/10 (Item 10 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00281100

SYSTEM FOR OVER-THE-AIR REPROGRAMMING OF COMMUNICATION RECEIVERS.

SYSTEM FUR DIE UBER-FUNK-REPROGRAMMIERUNG VON KOMMUNIKATIONSEMPFANGERN.

**SYSTEME DESTINE A LA REPROGRAMMATION RADIOELECTRIQUE DE RECEPTEURS DE
COMMUNICATIONS.**

PATENT ASSIGNEE:

MOTOROLA, INC., (205770), 1303 East Algonquin Road, Schaumburg, IL 60196,
(US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

DAVIS, Walter, Lee, 10948 N.W. 3rd Street, Coral Springs, FL 33071, (US)
SIRANG, Nasrin, 927-3 E. Golf Road, Arlington Heights, IL 60005, (US)

LEGAL REPRESENTATIVE:

Hudson, Peter David et al (52402), Motorola Patent and Licensing
Operations - Europe Jays Close Viabes Industrial Estate, Basingstoke
Hampshire RG22 4PD, (GB)

PATENT (CC, No, Kind, Date): EP 344149 A1 891206 (Basic)
EP 344149 B1 921007
WO 8805247 880714

APPLICATION (CC, No, Date): EP 87904334 870102; WO 87US4 870102

PRIORITY (CC, No, Date): EP 87904334 870102; WO 87US4 870102

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: H04Q-007/02; G08B-003/10;

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	844
CLAIMS B	(German)	EPBBF1	779
CLAIMS B	(French)	EPBBF1	1009
SPEC B	(English)	EPBBF1	13165
Total word count - document A			0
Total word count - document B			15797
Total word count - documents A + B			15797

...SPECIFICATION information begins with a 32 bit synchronization code word which is an **invariant** predetermined binary **sequence**. A batch that contains only **address** signals comprises a synchronization code word and sixteen address code words which...

4/3,K/11 (Item 11 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00236749

Public telephone system and apparatus.

Offentliches Fernsprechsystm und -Gerat.

Systeme publique de telephone et appareil.

PATENT ASSIGNEE:

URMET SUD S.p.A. Costruzioni Elettro-Telefoniche, (489490), Via di Castel
Romano, I-00129 Roma, (IT), (applicant designated states:
BE;CH;DE;ES;FR;GB;LI)

INVENTOR:

Mondardini, Massimo, Via Mazzini, 40, I-10100 Torino, (IT)

LEGAL REPRESENTATIVE:

Modiano, Guido et al (40782), c/o Modiano & Associati S.r.l. Via
Meravigli, 16, I-20123 Milano, (IT)

PATENT (CC, No, Kind, Date): EP 230283 A2 870729 (Basic)
EP 230283 A3 890426
EP 230283 B1 920930

APPLICATION (CC, No, Date): EP 87100489 870116;

PRIORITY (CC, No, Date): IT 8667047 860121

DESIGNATED STATES: BE; CH; DE; ES; FR; GB; LI

INTERNATIONAL PATENT CLASS: H04M-017/02;

ABSTRACT WORD COUNT: 133

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	2197
CLAIMS B	(German)	EPBBF1	1573
CLAIMS B	(French)	EPBBF1	1965
SPEC B	(English)	EPBBF1	4522
Total word count - document A			0
Total word count - document B			10257
Total word count - documents A + B			10257

...SPECIFICATION bus BB1, BB2. The CPU, by means of an internal data and address bus BI, is connected to a set of three memory banks M1, M2, M3, for collaboration therewith. The first bank M1 is composed of an EPROM...

4/3,K/12 (Item 1 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

00903176 **Image available**

TRANSACTION AUTHENTICATION

AUTHENTIFICATION DE TRANSACTIONS

Patent Applicant/Assignee:

BRITISH TELECOMMUNICATIONS PUBLIC LIMITED COMPANY, 81 Newgate Street,
London EC1A 7AJ, GB, GB (Residence), GB (Nationality), (For all
designated states except: US)

Patent Applicant/Inventor:

YATES Martin John, Sidehill, Spring Meadow, Playford, Ipswich, Suffolk
IP6 9ED, GB, GB (Residence), GB (Nationality), (Designated only for:
US)

THOMPSON Stephen Michael, 7 Borrett Place, Martlesham, Woodbridge,
Suffolk IP12 4TU, GB, GB (Residence), GB (Nationality), (Designated
only for: US)

EDWARDS Nicholas Hector, 85 Leopold Road, Ipswich, Suffolk IP4 4RN, GB,
GB (Residence), GB (Nationality), (Designated only for: US)

GIFFORD Maurice Merrick, 22 St. Agnes Way, Kesgrave, Ipswich, Suffolk IP5
1JZ, GB, GB (Residence), GB (Nationality), (Designated only for: US)

MCCARTNEY David John, 5 South Close, Ipswich, Suffolk IP4 2TH, GB, GB
(Residence), GB (Nationality), (Designated only for: US)

Legal Representative:

BRADLEY David William (agent), BT Group Legal Services, Intellectual
Property Department, 8th Floor, Holborn Centre, 120 Holborn, London
EC1N 2TE, GB,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200237241 A2-A3 20020510 (WO 0237241)

Application: WO 2001GB4836 20011101 (PCT/WO GB0104836)

Priority Application: EP 2000309635 20001101; GB 200122249 20010914

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI
SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 4670

Fulltext Availability:

Detailed Description

Detailed Description

... mobile terminal. The telephone number may be chosen pseudo-randomly
from a range of addresses.

Alternatively the vending system can display an invariant telephone
network address and a randomly generated password number to enter after
...

4/3,K/13 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

00831901 **Image available**

**A METHOD AND SYSTEM FOR DISCLOSING INFORMATION DURING ONLINE TRANSACTIONS
PROCEDE ET SYSTEME PERMETTANT DE PRESENTER DES INFORMATIONS PENDANT DES
TRANSACTIONS EN LIGNE**

Patent Applicant/Assignee:

ARETHUSA LIMITED, International House, Castle Hill, Victoria Road,
Douglas, Isle of Man, GB, GB (Residence), GB (Nationality), (For all
designated states except: US)

Patent Applicant/Inventor:

SKEA Alan, 1st floor, 57 Howitt Road, London NW3 4LU, GB, GB (Residence),
GB (Nationality), (Designated only for: US)

Legal Representative:

HOGG Jeffery Keith (et al) (agent), Withers & Rogers, Goldings House, 2
Hays Lane, London SE1 2HW, GB,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200165498 A1 20010907 (WO 0165498)

Application: WO 2001GB833 20010227 (PCT/WO GB0100833)

Priority Application: US 2000185624 20000229

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM
TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 15470

Fulltext Availability:

Claims

Claim

... information received from the service is encrypted.

38

. The improved user agent **set** forth in claim 12 wherein:
the network **address** is associated with the user agent-stored
transaction- **invariant** user information in the user agent.

14 The improved user agent set...

...information in the user agent such that the user agent-stored
transaction- **invariant** user information is locatable using the
associated network **address** .

15 The improved user agent **set** forth in claim 14 wherein:
the received user agent-stored transaction-invariant...stored
transaction-invariant user information maker associates the user
agent-stored transaction **invariant** user information with the network
address .

41

user acent.

30 The improved service **set** forth in claim 29 wherein:
the user agent-stored transaction- **invariant** user information maker
encrypts the user agent-stored transaction-invariant user information...
the network address.

48

AMENDED SHEET (ARTICLE 19)

, The improved user agent **set** forth in claim 12 wherein:
the network **address** is associated with the user agent-stored
transaction- **invariant** user information in the user agent.

14 The improved user agent set forth in claim 13 wherein:

the user agent-stored transaction- **invariant** user information is locatable using the associated network **address** .

15 The improved user agent **set** forth in claim 14 wherein: the received user agent-stored transaction-invariant...stored transaction-invariant user information maker associates the user agent-stored transaction **invariant** user information with the network **address** .

29 The improved service **set** forth in claim 27 wherein: the user agent-stored transaction- **invariant** user information maker includes the user agent-stored transaction-invariant user information...

4/3,K/16 (Item 5 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00160101

DIGITAL SELECTIVE CALLING RECEIVER HAVING A SELECTIVELY ATTACHABLE DEVICE
FOR MULTIPLE INDEPENDENT ADDRESS DECODING CAPABILITY
RECEPTEUR D'APPEL SELECTIF NUMERIQUE AYANT UN DISPOSITIF SELECTIVEMENTR
RATTACHABLE POUR FONCTION DE DECODAGE D'ADRESSES INDEPENDANTES
MULTIPLES

Patent Applicant/Assignee:
MOTOROLA INC,

Inventor(s):
DAVIS Walter Lee,

Patent and Priority Information (Country, Number, Date):

Patent: WO 8906475 A1 19890713

Application: WO 88US4396 19881209 (PCT/WO US8804396)

Priority Application: US 88455 19880104

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AT AU BE CH DE FR GB IT JP KR LU NL SE

Publication Language: English

Fulltext Word Count: 3261

Fulltext Availability:

Detailed Description

Detailed Description

... information

begins with a 32 bit synchronization code word which is an **invariant** predetermined binary **sequence** . A batch that contains only **address** signals comprises a synchronization code word and sixteen address code WO 89...